

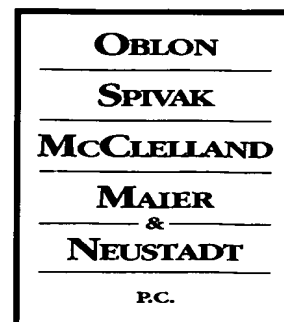


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Docket No.: 205109US20DIV

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313



ATTORNEYS AT LAW

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RE: Application Serial No.: 09/822,335

Applicants: Paul M. ENQUIST

Filing Date: April 2, 2001

For: SELF ALIGNED SYMMETRIC INTRINSIC
PROCESS AND DEVICE

Group Art Unit: 2814

Examiner: Ngan V. NGO

SIR:

Attached hereto for filing are the following papers:

**LETTER TO THE COMMISSIONER
INFORMATION DISCLOSURE STATEMENT
PTO-1449 (1)
CITED REFERENCES (12)**

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

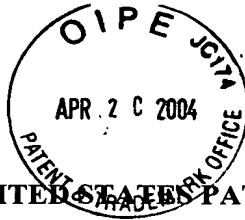
22850

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Docket No. 205109US20DIV



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Paul M. ENQUIST

SERIAL NO: 09/822,335

GAU: 2814

RCE FILED: July 24, 2003

EXAMINER: Ngan V. Ngo

FOR: SELF ALIGNED SYMMETRIC INTRINSIC PROCESS AND DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

Respectfully submitted,

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DOCKET NO: 205109US-20 DIV

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

RE APPLICATION OF

PAUL M. ENQUIST

SERIAL NO: 09/822,335

RCE FILED: JULY 24, 2003

FOR: SELF ALIGNED SYMMETRIC
INTRINSIC PROCESS AND DEVICE

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: EXAMINER: NGAN V. NGO

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: GROUP ART UNIT: 2814

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LETTER TO THE COMMISSIONER

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

The Applicant filed an Information Disclosure Statement (IDS) on April 2, 2001 listing references cited in the grandparent application (09/165,203 filed October 2, 1998, now U.S. 6,368,930) and parent application (09/311,149 filed May 13, 1999, now U.S. 6,242,794). On March 17, 2002 an Office Action issued where eight non-patent references were crossed out. In a recent discussion with Ex. Ngo, it was learned that the references were crossed out since copies of the references were not found in the grandparent application. Applicant's representative offered to supply copies for review, and Ex. Ngo agreed to review the references. The Applicant greatly appreciates the cooperation of Ex. Ngo. An IDS is filed herewith to have these references considered. Consideration of the eight references is proper, as set forth in MPEP §609(I)(A)(1-2). Also, it was explained in the discussion with Ex. Ngo that two other references cited in the grandparent, US 5,841,197 to Adamic, Jr. and US 5,185,274 to Chang, are believed to have been considered, as set forth in MPEP §609(I)(A)(1-2), and are listed on the attached IDS so that they may be printed on the patent.

Application No. 09/668,471
Letter to Commissioner

Lastly, two additional references, US 5,296,390 to Awano and US, 5,504,359 to Rodder, were cited in Application Serial No. 10/096,742, a continuation of the grandparent application examined by Ex. Lattin, and are included in the attached IDS.

Consideration of the references listed on the concurrently filed IDS is respectfully requested. Issuance of a Supplemental Notice of Allowability is also respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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| Form PTO 1449 (Modified) | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY DOCKET NO. 205109US20DIV | | SERIAL NO. 09/822,335 | |
| LIST OF REFERENCES CITED BY APPLICANT | | | | APPLICANT Paul M. ENQUIST | | | |
| | | | | FILING DATE RCE Filed July 24, 2003 | | GROUP 2814 | |
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| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
| | AA | 5,841,197 | 11/1998 | Adamic, Jr. | | | |
| | AB | 5,185,274 | 02/1993 | Chang et al. | | | |
| | AC | 5,296,390 | 03/1994 | Awano | | | |
| | AD | 5,504,359 | 04/1996 | Rodder | | | |
| | AE | | | | | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
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| | AQ | | | | | | |
| OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| | AR | Submicron Lateral Scaling of Vertical-Transport Devices: Transferred-Substrate Bipolar Transistors and Schottky-Collector Tunnel Diodes; M. Rodwell et al.; Invited Paper, 1997 OSA Ultrafast Electronics and Optoelectronics Conference | | | | | |
| | AS | A 277-GHz F_{max} Transferred-Substrate Heterojunction Bipolar Transistor; B. Agarwal et al.; IEEE Electron Device Letters, Vol. 18, No. 5, May 1997, pgs. 228-231 | | | | | |
| | AT | Transferred Substrate Schottky-Collector Heterojunction Bipolar Transistors: First Results and Scaling Laws for High F_{max} ; U. Bhattacharya, et al.; IEEE Electron Device Letters, Vol. 16, No. 8, August 1995, pgs. 357, 359 | | | | | |
| | AU | A > 400 Ghz F_{max} Transferred-Substrate HBT Integrated Circuit Technology; R. Pallela et al.; DRC 1997 | | | | | |
| | AV | Deep Submicron Transferred-Substrate Heterojunction Bipolar Transistors; Q. Lee et al.; DRC 1998 | | | | | |
| | AW | 170 GHz Transferred-Substrate Heterojunction Bipolar Transistor; U. Bhattacharya et al.; Electronics Letters, 18 th July 1996, vol. 32, No. 15, pgs. 1405-6 | | | | | |
| | AX | Transferred Substrate Heterojunction Bipolar Transistors; Dissertation by U. Bhattacharya, Nov. 27, 1996 | | | | | |
| | AY | Digital Integrated Circuits in the Transferred-Substrate HBT Technology, Dissertation by R. Pallela, June 1998 | | | | | |
| | AZ | | | | | <input type="checkbox"/> Additional References sheet(s) attached | |
| Examiner | | | | | Date Considered | | |
| *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |